

STLCMG Quarterly Meeting Agenda

Time	Topic	Presenter
8:30 - 8:50	Registration and Networking	
8:50 - 9:00	Welcome and Introductions	
9:00 – 10:00	Processor Consumption Analysis - Getting Started	Peter Enrico, EPS
10:15 – 11:15	Mainframe Performance Metrics and Observations in the Real World	Scott Chapman, EPS
11:30 – 12:00	<u>A Word from our Sponsor</u> Peter Enrico Enterprise Performance Strategies, Inc. (EPS)	
12:00 – 1:00	Lunch	
1:00 – 2:00	z/OS WLM - Revisiting Goals Over Time	Peter Enrico, EPS
2:15 – 3:15	More/Slower vs. Fewer/Faster CPUs: Practical Considerations	Scott Chapman, EPS
3:30	Closing Remarks PARS Location: O'Fallon Brewery 45 Progress Pkwy, Maryland Heights, MO 63043 Appetizers & Drinks!	

When :
May 7th, 2019

Location :
Edward Jones
 130 Edward Jones Blvd.
 Maryland Heights, MO, 63043

Cost:
 \$25

*Fee waived if you are out of work or a full-time college student with ID.
 St Louis CMG needs to receive RSVP 3 days prior to meeting*

Payment :

All payments can be made through the website using Chrome browser at: <https://st-louis-cmg.ticketleap.com/2019-spring-conference/>

For alternative payment arrangements contact: stlcmg@cmg.org

MEETING SPONSORED BY: ENTERPRISE PERFORMANCE STRATEGIES INC.

Performance Analysis Relaxation Sessions (PARS)

STLCMG will host PARS right after the meeting. Time to take a breather, network with other attendees and speakers, and just relax.

Enjoy complimentary hors d'oeuvres and two drinks.



Email: stlcmg@cmg.org

Future 2019 Meetings:

July 16th
 October 15th

ABSTRACTS AND BIO'S

Peter Enrico

Abstracts

[Processor Consumption Analysis - Getting Started](#)

The first step to any processor consumption analysis is to understand your processor configuration and settings. The next step is to understand what is consuming the fixed processor resource (and what is not allowed). It is only after understanding what and how the processor is being consumed can you conduct any sort of processor tuning or optimization exercise.

During this presentation, Peter Enrico shows you how to conduct the first critical steps to any processor resource consumption analysis. You are given a top down approach to better understand processor measurements available to help you gain a drill down insight into how the CPU resource is being consumed..

[z/OS WLM - Revisiting Goals Over Time](#)

There are many reasons to "open up" a WLM service definition. Some changes need to be made for Tuning, others for clean-up, and still others because the goals are outdated. Examination needs to be performed for planned environmental changes or changes to business priorities. The point is that there are many reasons to open up the WLM service definition for examination and potential changes.

During this presentation, Peter Enrico reviews the different reasons to roll up your sleeves and analyze your WLM service definition. During this presentation, you are sure to learn a lot about a wide variety of areas related to WLM management on z/OS.

Bio

Our founder and President, Peter brings a wealth of knowledge gained from his years working at IBM on the design and development of WLM and RMF as well as his years of experience helping customers solve their z/OS performance problems.

Scott Chapman

Abstracts

[Mainframe Performance Metrics and Observations in the Real World](#)

There is a plethora of z/OS performance metrics available. Some are easily understood and their applicability is well-known. Others are not so obvious. The more difficult question can be “what is a good value for this metric?” For some metrics, there are no “right” or “wrong” values as they are workload-dependent, but it is still interesting to understand the range of values commonly seen. Situations that generate metrics outside the normal range might be worth investigating.

Join Scott Chapman for a tour through some interesting z/OS performance metrics, both familiar and not-so-familiar. He will provide a sense for what values other customers are seeing for these metrics as well as some modern rules of thumb where applicable.

[More/Slower vs. Fewer/Faster CPUs: Practical Considerations](#)

Mainframe customers have had a choice of processor speeds for years. This flexibility has been somewhat limited by the number of sub-capacity engines that can be configured. But now that dozens of sub-capacity engines can be used in a single machine, more sites should consider them. What are the advantages to using sub-capacity engines? Does your LPAR configuration matter? What metrics should be examined to determine if more/slower CPUs is a good fit for your workload? Is it possible that this choice might impact your software bill?

Come to this session with Scott Chapman to learn why more/slower CPUs may be a better fit for many environments and how to determine what workloads might be at risk for moving to slower CPUs.

Bio

Scott Chapman is the Director of Software Design and Development of Enterprise Performance Strategies, Inc. Along with Peter Enrico, Scott is the co-designer and lead developer for Pivotor®, a web based z/OS SMF data mining, reporting, and analysis solution. Scott is an amazing z/OS performance expert, performance workshop instructor, conference presenter, and a very clever performance analyst. Scott is CMG's 2009 Mullen Award winner, a winner of SHARE best presentation award, and is currently Chairman for the Ohio Valley CMG.